	Application No.	Applicant(s)
Notice of Allowability	10/692,079	CASTLEBARY ET AL.
	Examiner	Art Unit	
	Lawrence B. Williams	2611	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to *amendment filed on 05 January 2007*.

2. ☒ The allowed claim(s) is/are *1-8*.

3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

    a) ☐ All   b) ☐ Some*   c) ☐ None   of the:

        1. ☐ Certified copies of the priority documents have been received.

        2. ☐ Certified copies of the priority documents have been received in Application No. _____ .

        3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

    * Certified copies not received: _____ .

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. **THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.

    (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached

        1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____ .

    (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of

        Paper No./Mail Date _____ .

    Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)

2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)

3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date _____

4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material

5. ☐ Notice of Informal Patent Application

6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____ .

7. ☒ Examiner's Amendment/Comment

8. ☒ Examiner's Statement of Reasons for Allowance

9. ☐ Other _____ .

DRAWINGS

1.   This application has been filed with informal drawings, which are acceptable for

examination purposes only. Formal drawings will be required when the application is allowed.

REASONS FOR ALLOWANCE

1.   The following is an examiner's statement of reasons for allowance: The instant

application discloses a method and apparatus for sampling a digital signal yielding improved

jitter performance. A search of prior art records has failed to teach or suggest, alone or in

combination:

"a method for sampling a digital signal yielding improved jitter performance within

prescribed bandwidth constraints, comprising the steps of: periodically sampling the digital

signal n times during every interval t, with n chosen such that $\log_2(n+1)$ is an integer (x) greater

than zero; generating a x+1-bit sample value after each interval t, the sample value having a first

bit indicating the value of the digital signal being sampled, and x remaining bits which

collectively indicate a sample interval during which the digital signal changed states if such a

change did occur, and inverting the first bit of each sample value upon decoding to coincide with

the change in the digital signal" as disclosed in claim 1.

"a method for sampling a digital signal yielding improved jitter performance within

prescribed bandwidth constraints, comprising the steps of: periodically sampling the digital

signal n times during every interval t, with n chosen such that $\log_2(n) =< x$ where x is an integer;

generating a x+1-bit sample value after each interval t, the sample value having a first bit

indicating the value of the digital signal being sampled, and x remaining bits which collectively

indicate a sample interval during which the digital signal changed states if such a change did

occur, and inverting the first bit of each sample value upon decoding to coincide with the change

in the digital signal" as disclosed in claim 3.

" an apparatus for sampling a digital signal yielding improved jitter performance within

prescribed bandwidth constraints, comprising of: a sample clock for generating n periodic clock pulses during every interval t, with n chosen such that $\log_2(n+1)$ is an integer (x) greater than zero; a receiver for generating a x+1-bit sample value after each interval t, the sample value having a first bit indicating the value of the digital signal being sampled, and x remaining bits which collectively indicate a sample interval during which the digital signal changed states if such a change did occur, and the receiver inverting the first bit of each sample value upon decoding to coincide with the change in the digital signal" as disclosed in claim 5.

" an apparatus for sampling a digital signal yielding improved jitter performance within prescribed bandwidth constraints, comprising of: a sample clock for generating n periodic clock pulses during every interval t, with n chosen such that $\log_2(n) =< x$ where x is an integer a receiver for generating a x+1-bit sample value after each interval t, the sample value having a first bit indicating the value of the digital signal being sampled, and x remaining bits which collectively indicate a sample interval during which the digital signal changed states if such a change did occur, and the receiver inverting the first bit of each sample value upon decoding to coincide with the change in the digital signal" as disclosed in claim 7.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

CONCLUSION

2.   Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Lawrence B Williams whose telephone number is 571-272-3037.

The examiner can normally be reached on Monday-Friday (8:00-6:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Ghayour Mohammad can be reached on 571-272-3021. The fax phone number for

the organization where this application or proceeding is assigned is 571-273-8300.
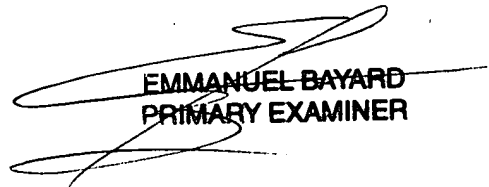
Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Lawrence B. Williams

lbw
February 8, 2007

EMMANUEL BAYARD
PRIMARY EXAMINER